EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S59	8	Rapidchip with (dic$3 slic$3 singulat$3 cut) and silicon	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 15:17
S60	43645	"field programmable gate array" "FPGA" "field programmable platform array" "FPPA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 16:52
S61	602551	(cut$3 dic$3 singulat$3 slic$3) and (silicon Si)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 15:22
S62	2153	S60 and S61	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 15:22
S63	739	S62 and (core and processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 15:23
S64	469	S63 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 15:25
S65	381	("field programmable gate array" "FPGA" "field programmable platform array" "FPPA") with (slic$3 cut$3 singulat$3 dic$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 15:25

S66	284	S65 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:56
S67	0	((die or chip) with dicing with processors with cores) and "field programmable"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 16:44
S68	0	((die or chip) with dicing with processors with cores)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 16:44
S69	7	((die or chip) with dic$3 with (processors and core))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 16:51
S70	0	((die or chip) with dic$3 with ("platform array"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 16:52
S71	253	(die or chip) with dic$3 and ("field programmable gate array" "FPGA" "field programmable platform array" "FPPA" "platform array" PAU)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 16:54
S72	62	(singulat$3 dic$3) with ("field programmable gate array" "FPGA" "field programmable platform array" "FPPA" "platform array" PAU)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 17:00

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S73	50770	("field programmable gate array" "FPGA" "field programmable platform array" "FPPA" "platform array" PAU)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 17:23
S74	18	S73 and (processor and core) and (pre$1rout$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 17:02
S76	4	"field programmable platform array"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/24 17:23
S77	381	("field programmable gate array" "FPGA" "field programmable platform array" "FPPA") with (slic$3 cut$3 singulat$3 dic$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:27
S78	381	("field programmable gate array" "FPGA" "field programmable platform array" "FPPA") with (slic$3 cut$3 singulat$3 dic$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:55
S79	53	S78 and (silicon Si) and (metal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:38
S80	435	("field programmable gate array" "FPGA" "field programmable platform array" "FPPA" "PAU" "platform array") with (slic$3 cut$3 singulat$3 dic$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:28

S81	2	("6953956").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/25 16:37
S82	63	S80 and (silicon Si) and (metal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:43
S83	2	("20010044918").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/25 16:43
S84	25	(US-20040060032-$ or US-20020072135-$ or US-20050062495-$ or US-20040161878-$ or US-20040061147-$ or US-20040119098-$ or US-20020125910-$ or US-20020079921-$ or US-20010044918-$ or US-20060081971-$).did. or (US-6910201-$ or US-6962829-$ or US-5581742-$ or US-5581738-$ or US-6362651-$ or US-6953956-$ or US-6222212-$ or US-6157213-$ or US-5822256-$ or US-7105871-$ or US-7020860-$ or US-6600495-$ or US-7007250-$ or US-6983439-$ or US-6769107-$).did.	US-PGPUB; USPAT	OR	ON	2007/07/25 16:52
S85	0	S84 and (polysilicon near2 wafer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:53

S86	5	S84 and (silicon near2 wafer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:53
S87	633	("field programmable gate array" "FPGA" "field programmable platform array" "FPPA" "platform array" "PLD" "programmable logic") and (processor and core) and (silicon Si) with (wafer substrate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 16:56
S88	378	S87 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:42
S89	2	("20010038297").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/25 19:18
S90	1	"unit specialization" and "unit role assignment" and communication	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 19:34
S91	102	"storage area network" and wafer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 19:50
S92	35912	("storage area network" SAN) and wafer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 19:36

S93	12	"storage area network" with (FPGA "field programmable gate array")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/25 19:51
S94	2	("20020091977").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/26 11:05

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	29	(US-20020072135-$ or US-20020079921-$ or US-20010044918-$ or US-20040060032-$ or US-20020125910-$ or US-20060081971-$ or US-20050062495-$ or US-20040119098-$ or US-20040061147-$ or US-20040161878-$ or US-20030206036-$ or US-20020043988-$).did. or (US-6910201-$ or US-7032190-$ or US-6962829-$ or US-5581742-$ or US-5581738-$ or US-6996758-$ or US-6600495-$ or US-7007250-$ or US-6983439-$ or US-6769107-$ or US-6362651-$ or US-6222212-$ or US-6157213-$ or US-7020860-$ or US-6953956-$ or US-7105871-$ or US-5822256-$).did.	US-PGPUB; USPAT	OR	ON	2007/07/26 13:18
L5	5	4 and (aluminium and copper)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:18
L6	549	(copper near3 layer) with encapsulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:39
L7	3	6 and (FPGA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:40
L8	0	(FPGA "field programmable gate array") same (copper with (via pad interconnect$3)) with encapsulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:41

L9	501	(copper with (via pad interconnect$3)) with encapsulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:48
L10	405	9 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 13:42
L11	54	chip and die and (copper with (pad ball)) with encapsulat$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/26 14:07
L12	2	("20010038297").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/26 14:07